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APPLICATION NO.	PLICATION NO. FILING DATE		FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/942,198	(08/29/2001	Kun-Yung Ken Chang	RB1-038US	6966	
29150	7590	02/23/2005		EXAMINER		
LEE & HAYES, PLLC 421 W. RIVERSIDE AVE, STE 500				VARTANIAN, HARRY		
SPOKANE, WA 99201				ART UNIT	PAPER NUMBER	
 ,				2634	·	

DATE MAILED: 02/23/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

	<u>:</u>	Applicatio	n No.	Applicant(s)						
		09/942,19	8	CHANG ET AL.						
•	Office Action Summary	Examiner		Art Unit						
		Harry Vart	anian	2634						
The MAILING DATE of this communication appears on the cover sheet with the correspondence address										
THE - Exte after - If the - If NO - Failu Any earn	ORTENED STATUTORY PERIOD FOR REP MAILING DATE OF THIS COMMUNICATION nsions of time may be available under the provisions of 37 CFR SIX (6) MONTHS from the mailing date of this communication. period for reply specified above is less than thirty (30) days, a red period for reply is specified above, the maximum statutory perior to reply within the set or extended period for reply will, by statication received by the Office later than three months after the mailed patent term adjustment. See 37 CFR 1.704(b).	1.136(a). In no eve eply within the statu d will apply and will ute, cause the appli	nt, however, may a reply be time tory minimum of thirty (30) days expire SIX (6) MONTHS from to cation to become ABANDONEC	ely filed will be considered timel he mailing date of this c	ly. ommunication.					
Status										
1)⊠	Responsive to communication(s) filed on 29	<u>August 2001</u> .								
2a) <u></u>	This action is FINAL . 2b)⊠ Th	nis action is no	on-final.							
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.									
Disposit	ion of Claims		·							
5)⊠ 6)⊠ 7)⊠	Claim(s) 1-24 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. Claim(s) 6-24 is/are allowed. Claim(s) 1-4 is/are rejected. Claim(s) 5 is/are objected to. Claim(s) are subject to restriction and/or election requirement.									
Applicat	ion Papers									
10)⊠	The specification is objected to by the Examination The drawing(s) filed on 29 August 2001 is/are Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct The oath or declaration is objected to by the	e: a) accep ne drawing(s) be ection is require	e held in abeyance. See ed if the drawing(s) is obj	37 CFR 1.85(a). ected to. See 37 C	FR 1.121(d).					
Priority (under 35 U.S.C. § 119									
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 										
Attachmen	t(s)									
	e of References Cited (PTO-892)		4) Interview Summary (
3) Infor	e of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/0 r No(s)/Mail Date	8)	Paper No(s)/Mail Dai 5) Notice of Informal Pa 6) Other:		O-152)					

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DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 1. Claims 1-4 are rejected under 35 U.S.C. 102(b) as being anticipated by Yamane et (United States Patent# 4,965,797). Yamane et al discloses a parallel-to-serial converter for use in high-speed data networks. Regarding Claim 1, Yamane et al meets the following limitations of the Claim:

providing a system clock signal associated with data in a first clock domain; **Abstract; See figure 3** "input clock"

providing a high speed clock signal relative to the system clock signal; Abstract; fig 3, item 33

dividing the high speed clock signal to provide a clock signal in a second **Abstract; fig 3, item 34** clock domain; and

clocking data using the clock signal in the second clock domain. **Abstract**

Regarding Claim 2, Yamane et al meets the following limitations of the Claim:

providing a high speed clock signal that is N times faster than the system clock signal. (Column 2, Line 61 to Column 3, Line 7)

Regarding Claim 4, Yamane et al meets the following limitations of the Claim:

providing the system clock signal at a first frequency; Abstract

said dividing the high speed clock signal comprises dividing the high speed clock signal such that the clock signal in the second clock domain is provided at the first frequency. **Abstract**

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject

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matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

The factual inquiries set forth in *Graham* **v.** *John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

- 1. Determining the scope and contents of the prior art.
- 2. Ascertaining the differences between the prior art and the claims at issue.
- 3. Resolving the level of ordinary skill in the pertinent art.
- 4. Considering objective evidence present in the application indicating obviousness or nonobviousness.
- 2. Claim 3 is rejected under 35 U.S.C. 103(a) as being unpatentable over Yamane et al (United States Patent# 4,965,797). Yamane et al meets all the limitations of Claim 3, except disclosing that the "high-speed" serial clock is 4 or more times greater than the system clock.

At the time the invention was made, it would have been obvious to a person of ordinary skill in the art to use a high-speed clock that is 4 or more times greater than the system clock. Applicant has not disclosed that using a high-speed clock that is 4 or more times greater than the system clock provides an advantage, is used for a particular purpose, or solves a stated problem. Therefore, it would have been obvious to one of ordinary skill in this art to modify Yamane et al to obtain the invention as specified in claim 3.

Allowable Subject Matter

3. Claim 5 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

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4. Claims 6-24 are allowed.

The following is an examiner's statement of reasons for allowance: The specific use of the

clocked reset signal in the parallel to serial conversion system was not taught by the prior

art.

Any comments considered necessary by applicant must be submitted no later than

the payment of the issue fee and, to avoid processing delays, should preferably accompany

the issue fee. Such submissions should be clearly labeled "Comments on Statement of

Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Harry Vartanian whose telephone number is 571.272.3048.

The examiner can normally be reached on 10:00-6:30 Mondays to Fridays.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Stephen Chin can be reached on 571.272.3056. The fax phone number for the

organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published

applications may be obtained from either Private PAIR or Public PAIR. Status information

for unpublished applications is available through Private PAIR only. For more information

about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on

access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-

217-9197 (toll-free).

Harry Vartanian

Examiner

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STEPHEN CHIN

SUPERVISORY PATENT EXAMINE

TECHNOLOGY CENTER 2600

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